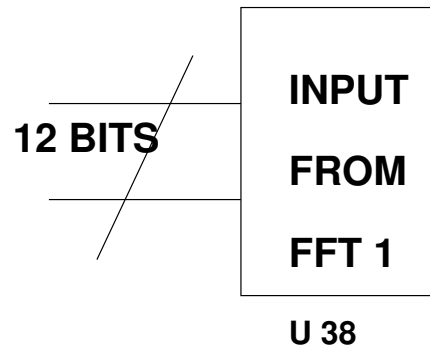


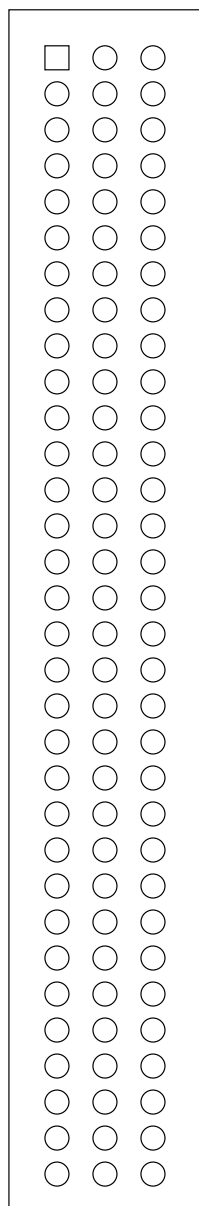
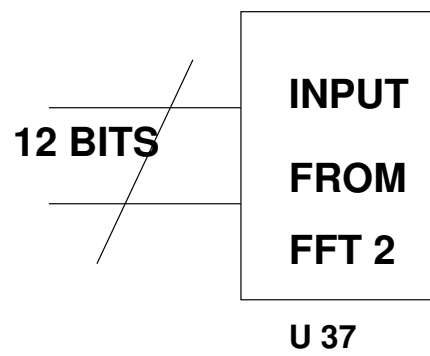
BLOCK DIAGRAM OF TRANSLATE CARD.



4 REAL BITS

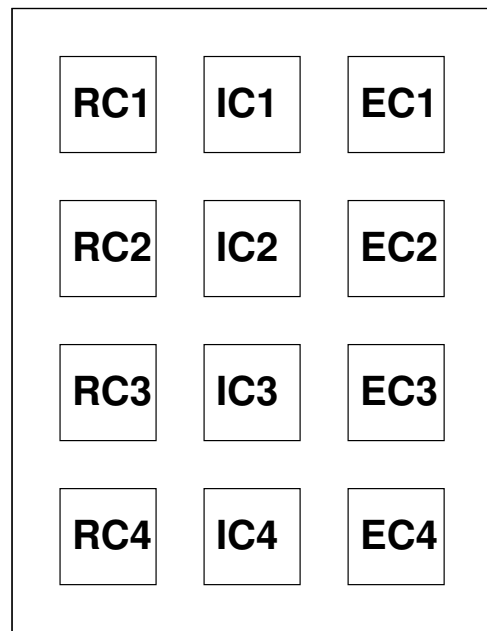
4 IMAGINARY &

4 COMM. EXPONENT BITS.

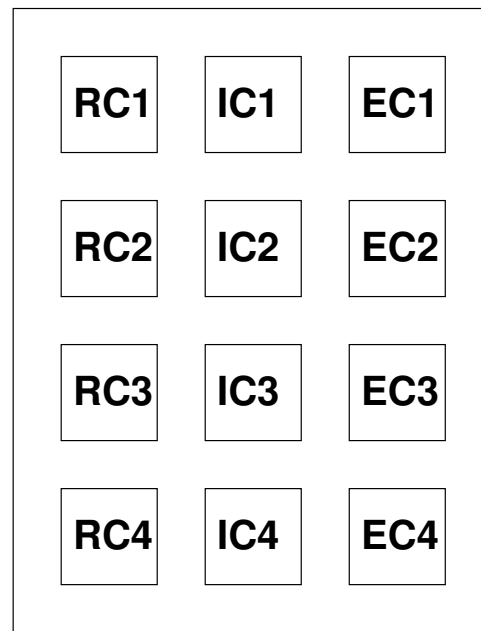


96 p EURO CONN'R.

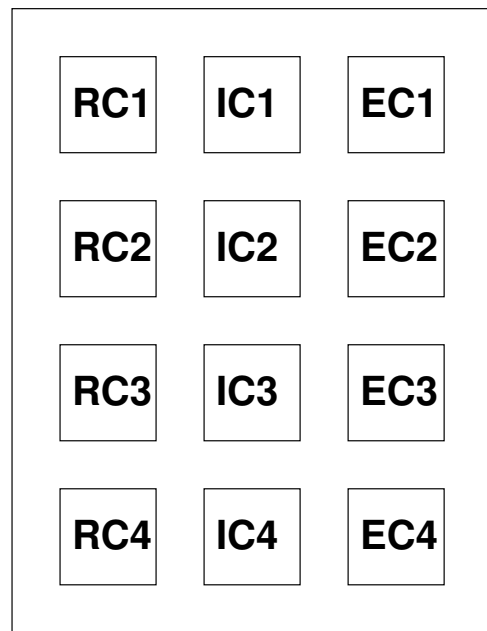
FOR I/P & O/P signals.



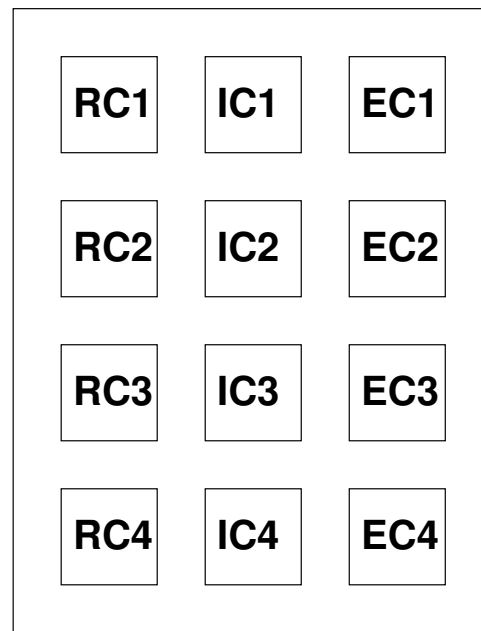
10124 PLCC



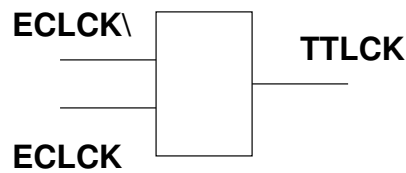
10116 PLCC



10124 PLCC



10116 PLCC

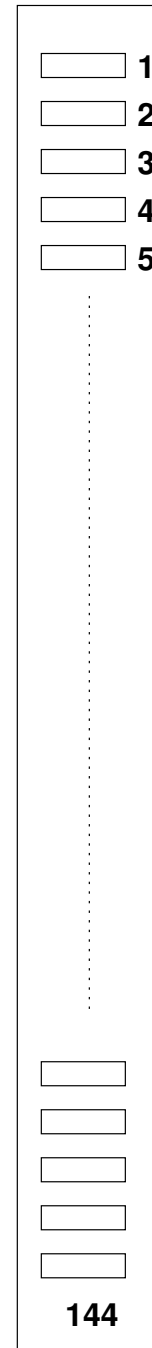


R – REAL I – IMAGINARY E – EXPONENT C – COPY NUMBER

Prepared by : Shri IRAPPA M. HALAGALI

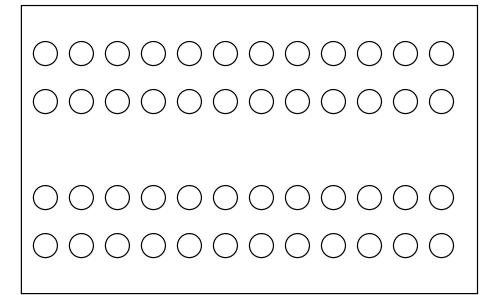
Dated : 12.12.2003

Series termination chip resistors of 39 ohm.

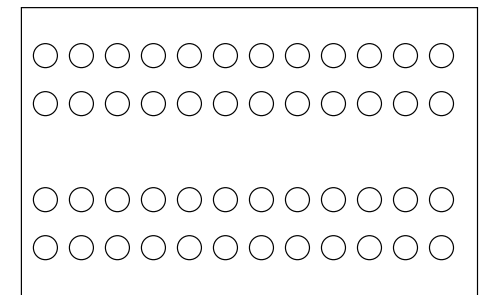


48 PIN SMD CONNECTORS ON TRANSLATE BACKPLANE.
3 COPIES FOR FX RACKS.

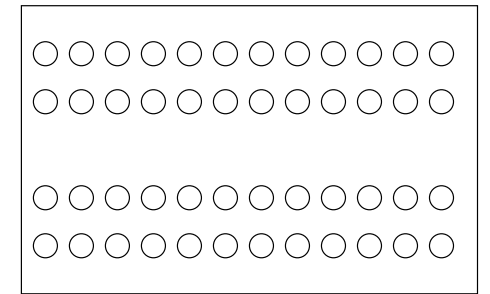
FIRST COPY



SECOND COPY



THIRD COPY



50 p FRC CONN'R. FOURTH COPY TO GAC

